

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	3275305	memory or storage	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/11/16 14:26
L2	1375	1 and ("3 "or "three") adj3 transistor with cell	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/11/16 14:31
L3	479	2 and (nonvolatile or non-volatile or floating adj gate)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/11/16 14:31
L4	197	3 and sense adj amplifier and eras\$5	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/11/16 14:44
L5	52	4 and (block or bank or array or subarray) with byte	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/11/16 14:44